

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application:

Application No.:

Filed:

Title:

Commissioner for patents
Washington, D.C. 20231POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST
(REVOCATION OF PRIOR POWERS)

As assignee of record of each of the patent applications listed in the table of Exhibit A (and all issued patents listed in the table of Exhibit B),

REVOCATION OF PRIOR POWERS OF ATTORNEY

all powers of attorney previously given in each of the patent applications listed in Exhibit A, and all of the issued patents listed in Exhibit B, are hereby revoked, and

NEW POWER OF ATTORNEY

the attorneys of Thomas, Kayden, Horstemeyer & Risley, LLP, who are listed under the USPTO Customer Number shown below are appointed as the attorneys to prosecute these applications and to transact all business in the United States Patent and Trademark Office connected therewith; recognizing that the specific attorneys listed under that Customer Number may be changed from time to time at the sole discretion of Thomas, Kayden, Horstemeyer & Risley, LLP, and request that all correspondence be addressed to the address filed under the same USPTO Customer Number.

24504

Please direct all correspondence and telephone calls to:

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1

ASSIGNEE OF ENTIRE INTEREST

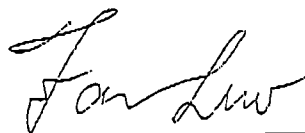
AU OPTRONICS CORP.
No. 1, Li-Hsin Rd. 2,
Science-Based Industrial Park
Hsinchu 300, Taiwan, R.O.C.

ASSIGNEE CERTIFICATION

The certification under 37 C.F.R. §3.73(b) establishing the right of assignee to take action is attached hereto along with documentation evidencing same. Further, in my official position with AU Optronics Corp., I am authorized to sign documents and otherwise act on its behalf in connection with the management and handling of patent applications and other intellectual property matters.

Date:

12/20/2004



Name Fan LUO
Title V.P. of CTO

Exhibit A
AU Optronics Corp. Patent Applications
Subject to Revocation of Power of Attorney by Assignee

No.	Serial No.	Title	Filing Date	Assignment (Reel/Frame)
1	10/606,860	Power-Saving Circuits and Methods for Driving Active Matrix Display Elements	6/27/03	014253/0773
2	10/727,616	Reflector of Backlight Assembly for Liquid Crystal Display Device	12/5/03	014767/0553
3	10/785,100	Design Methodology of Power Supply Lines in Electroluminescence Display	2/25/04	015020/0318
4	10/747,407	Bendable Light Guide	12/30/03	014855/0158
5	10/785,099	Liquid Crystal Display Device Pixel and Drive Circuit	2/25/04	015020/0662
6	10/747,068	Low Power Driving in a Liquid Crystal Display	12/30/03	014855/0028
7	10/747,406	Mobile Unit with Style Panel Display	12/30/03	014855/0070
8	10/738,040	Dual Display Panel	12/18/03	014810/0353
9	10/865,940	Redundant Storage Capacitor and Method for Repairing OLED Pixels and Driving	6/14/04	015465/0439
10	11/005,648	Storage Capacitor in OLED Pixels and Driving Circuits and Method for Forming the Same	12/7/04	Assignment Provided
11	10/761,211	Analog Buffer for ITPS AMLCD	1/22/04	014914/0888
12	10/833,143	Current-Driven OLED Pixel	4/28/04	015275/0502
13	10/776,177	OLED Pixel	2/12/04	014982/0975
14	10/975,370	Integrated Circuit for Driving Liquid Crystal Display Device	10/29/04	Assignment Provided
15	10/865,941	Integrated Circuit for Liquid Crystal Display Device	6/14/04	015465/0552
16	10/857,860	Driving Method for Dual Panel Display	6/2/04	015426/0311
17	10/911,483	Dual Emitting Method and Device for Active Matrix Organic Electroluminescence	8/5/04	Assignment Provided

A-1